

LOC	OBJ	LINE	SOURCE
F77D E947FA		4738	JMP VIDEO_RETURN ; RETURN TO CALLER
		4739	
F780		4740	U6: ; SET-CURSOR-INC
F780 FEC6		4741	INC DH ; NEXT ROW
F782		4742	U7: ; SET-CURSOR
F782 B402		4743	MOV AH,2
F784 EBF4		4744	JMP U4 ; ESTABLISH THE NEW CURSOR
		4745	
		4746	;------ BACK SPACE FOUND
		4747	
F786		4748	U8:
F786 80FA00		4749	CMP DL,0 ; ALREADY AT END OF LINE
F789 74F7		4750	JE U7 ; SET_CURSOR
F78B FECA		4751	DEC DL ; NO -- JUST MOVE IT BACK
F78D EBF3		4752	JMP U7 ; SET_CURSOR
		4753	
		4754	;------ CARRIAGE RETURN FOUND
		4755	
F78F		4756	U9:
F78F B200		4757	MOV DL,0 ; MOVE TO FIRST COLUMN
F791 EBEF		4758	JMP U7 ; SET_CURSOR
		4759	
		4760	;------ LINE FEED FOUND
		4761	
F793		4762	U10:
F793 80FE18		4763	CMP DH,24 ; BOTTOM OF SCREEN
F796 75E8		4764	JNE U6 ; YES, SCROLL THE SCREEN
F798 EBB9		4765	JMP U1 ; NO, JUST SET THE CURSOR
		4766	
		4767	;------ BELL FOUND
		4768	
F79A		4769	U11:
F79A B302		4770	MOV BL,2 ; SET UP COUNT FOR BEEP
F79C E8C7EE		4771	CALL BEEP ; SOUND THE POD BELL
F79F EBD8		4772	JMP US ; TTY_RETURN
		4773	WRITE_TTY ENDP
		4774	;------
		4775	; LIGHT PEN
		4776	; THIS ROUTINE TESTS THE LIGHT PEN SWITCH AND THE LIGHT
		4777	; PEN TRIGGER. IF BOTH ARE SET, THE LOCATION OF THE LIGHT
		4778	; PEN IS DETERMINED. OTHERWISE, A RETURN WITH NO INFORMATION
		4779	; IS MADE.
		4780	; ON EXIT:
		4781	; (AH) = 0 IF NO LIGHT PEN INFORMATION IS AVAILABLE
		4782	; BX,CX,DX ARE DESTROYED
		4783	; (AH) = 1 IF LIGHT PEN IS AVAILABLE
		4784	; (DH,DL) = ROW,COLUMN OF CURRENT LIGHT PEN POSITION
		4785	; (CH) = RASTER POSITION
		4786	; (BX) = BEST GUESS AT PIXEL HORIZONTAL POSITION
		4787	;------
		4788	ASSUME CS:CODE,DS:DATA
		4789	;------ SUBTRACT_TABLE
F7A1		4790	V1 LABEL BYTE
F7A1 0303050503030304		4791	DB 3,3,5,5,3,3,4 ;
F7A9		4792	READ_LPEN PROC NEAR
		4793	
		4794	;------ WAIT FOR LIGHT PEN TO BE DEPRESSED
		4795	
F7A9 B400		4796	MOV AH,0 ; SET NO LIGHT PEN RETURN CODE
F7AB 8B166300	R	4797	MOV DX,ADDR_6845 ; GET BASE ADDRESS OF 6845
F7AF 83C206		4798	ADD DX,6 ; POINT TO STATUS REGISTER
F7B2 EC		4799	IN AL,DX ; GET STATUS REGISTER
F7B3 A804		4800	TEST AL,4 ; TEST LIGHT PEN SWITCH
F7B5 7578		4801	JNZ V6 ; NOT SET, RETURN
		4802	
		4803	;------ NOW TEST FOR LIGHT PEN TRIGGER
		4804	
F7B7 A802		4805	TEST AL,2 ; TEST LIGHT PEN TRIGGER
F7B9 747E		4806	JZ V7 ; RETURN WITHOUT RESETTING TRIGGER
		4807	
		4808	;------ TRIGGER HAS BEEN SET, READ THE VALUE IN
		4809	
F7BB B410		4810	MOV AH,16 ; LIGHT PEN REGISTERS ON 6845
		4811	
		4812	;------ INPUT REGS POINTED TO BY AH, AND CONVERT TO ROW COLUMN IN DX
		4813	
F7BD 8B166300	R	4814	MOV DX,ADDR_6845 ; ADDRESS REGISTER FOR 6845

LOC	OBJ	LINE	SOURCE
F7C1	8AC4	4815	MOV AL,AH ; REGISTER TO READ
F7C3	EE	4816	OUT DX,AL ; SET IT UP
F7C4	42	4817	INC DX ; DATA REGISTER
F7C5	EC	4818	IN AL,DX ; GET THE VALUE
F7C6	8AE8	4819	MOV CH,AL ; SAVE IN CX
F7C8	4A	4820	DEC DX ; ADDRESS REGISTER
F7C9	FEC4	4821	INC AH
F7CB	8AC4	4822	MOV AL,AH ; SECOND DATA REGISTER
F7CD	EE	4823	OUT DX,AL
F7CE	42	4824	INC DX ; POINT TO DATA REGISTER
F7CF	EC	4825	IN AL,DX ; GET SECOND DATA VALUE
F7D0	8AE5	4826	MOV AH,CH ; AX HAS INPUT VALUE
		4827	
		4828	;----- AX HAS THE VALUE READ IN FROM THE 6845
		4829	
F7D2	8A1E4900	R 4830	MOV BL,CRT_MODE
F7D6	2AFF	4831	SUB BH,BH ; MODE VALUE TO BX
F7D8	2E8A9FA1F7	R 4832	MOV BL,CS:V1[BX] ; DETERMINE AMOUNT TO SUBTRACT
F7D0	2BC3	4833	SUB AX,BX ; TAKE IT AWAY
F7D0	2B064E00	R 4834	SUB AX,CRT_START ; CONVERT TO CORRECT PAGE ORIGIN
F7E3	7903	4835	JNS V2 ; IF POSITIVE, DETERMINE MODE
F7E5	B80000	4836	MOV AX,0 ; <0 PLAYS AS 0
		4837	
		4838	;----- DETERMINE MODE OF OPERATION
		4839	
F7E8		4840	V2: ; DETERMINE MODE
F7E8	B103	4841	MOV CL,3 ; SET #8 SHIFT COUNT
F7EA	803E490004	R 4842	CMP CRT_MODE,4 ; DETERMINE IF GRAPHICS OR ALPHA
F7EF	722A	4843	JB V4 ; ALPHA_PEN
F7F1	803E490007	R 4844	CMP CRT_MODE,7
F7F6	7423	4845	JE V4 ; ALPHA_PEN
		4846	
		4847	;----- GRAPHICS MODE
		4848	
F7F8	B228	4849	MOV DL,40 ; DIVISOR FOR GRAPHICS
F7FA	F6F2	4850	DIV DL ; DETERMINE ROW(AL) AND COLUMN(AH)
		4851	; AL RANGE 0-99, AH RANGE 0-39
		4852	;----- DETERMINE GRAPHIC ROW POSITION
		4853	
F7FC	8AE8	4854	MOV CH,AL ; SAVE ROW VALUE IN CH
F7FE	02ED	4855	ADD CH,CH ; #2 FOR EVEN/ODD FIELD
F800	8ADC	4856	MOV BL,AH ; COLUMN VALUE TO BX
F802	2AFF	4857	SUB BH,BH ; MULTIPLY BY 8 FOR MEDIUM RES
F804	803E490006	R 4858	CMPI CRT_MODE,6 ; DETERMINE MEDIUM OR HIGH RES
F809	7504	4859	JNE V3 ; NOT_HIGH_RES
F80B	B104	4860	MOV CL,4 ; SHIFT VALUE FOR HIGH RES
F80D	D0E4	4861	SAL AH,1 ; COLUMN VALUE TIMES 2 FOR HIGH RES
F80F		4862	V3: ; NOT_HIGH_RES
F80F	D3E3	4863	SHL BX,CL ; MULTIPLY #16 FOR HIGH RES
		4864	
		4865	;----- DETERMINE ALPHA CHAR POSITION
		4866	
F811	8AD4	4867	MOV DL,AH ; COLUMN VALUE FOR RETURN
F813	8AF0	4868	MOV DH,AL ; ROW VALUE
F815	D0EE	4869	SHR DH,1 ; DIVIDE BY 4
F817	D0EE	4870	SHR DH,1 ; FOR VALUE IN 0-24 RANGE
F819	EB12	4871	JMP SHORT V5 ; LIGHT_PEN_RETURN_SET
		4872	
		4873	;----- ALPHA MODE ON LIGHT PEN
		4874	
F81B		4875	V4: ; ALPHA_PEN
F81B	F6364A00	R 4876	DIV BYTE PTR CRT_COLS ; DETERMINE ROW,COLUMN VALUE
F81F	8AF0	4877	MOV DH,AL ; ROWS TO DH
F821	8AD4	4878	MOV DL,AH ; COLS TO DL
F823	D2E0	4879	SAL AL,CL ; MULTIPLY ROWS * 8
F825	8AE8	4880	MOV CH,AL ; GET RASTER VALUE TO RETURN REG
F827	8ADC	4881	MOV BL,AH ; COLUMN VALUE
F829	32FF	4882	XOR BH,BH ; TO BX
F82B	D3E3	4883	SAL BX,CL
F82D		4884	V5: ; LIGHT_PEN_RETURN_SET
F82D	B401	4885	MOV AH,1 ; INDICATE EVERYTHING SET
F82F		4886	V6: ; LIGHT_PEN_RETURN
F82F	52	4887	PUSH DX ; SAVE RETURN VALUE (IN CASE)
F830	8B166300	R 4888	MOV DX,ADDR_6845 ; GET BASE ADDRESS
F834	83C207	4889	ADD DX,7 ; POINT TO RESET PARAM
F837	EE	4890	OUT DX,AL ; ADDRESS, NOT DATA, IS IMPORTANT

LOC	OBJ	LINE	SOURCE
F838 5A		4891	POP DX ; RECOVER VALUE
F839		4892	V7: ; RETURN_NO_RESET
F839 5F		4893	POP DI
F83A 5E		4894	POP SI
F83B 1F		4895	POP DS ; DISCARD SAVED BX,CX,DX
F83C 1F		4896	POP DS
F83D 1F		4897	POP DS
F83E 1F		4898	POP DS
F83F 07		4899	POP ES
F840 CF		4900	IRET
		4901	READ_LPEN ENDP
		4902	;--- INT 12 -----
		4903	; MEMORY_SIZE_DETERMINE
		4904	; THIS ROUTINE DETERMINES THE AMOUNT OF MEMORY IN THE SYSTEM
		4905	; AS REPRESENTED BY THE SWITCHES ON THE PLANAR. NOTE THAT
		4906	; THE SYSTEM MAY NOT BE ABLE TO USE I/O MEMORY UNLESS THERE
		4907	; IS A FULL COMPLEMENT OF 64K BYTES ON THE PLANAR.
		4908	; INPUT
		4909	; NO REGISTERS
		4910	; THE MEMORY_SIZE VARIABLE IS SET DURING POWER ON DIAGNOSTICS
		4911	; ACCORDING TO THE FOLLOWING HARDWARE ASSUMPTIONS:
		4912	; PORT 60 BITS 3,2 = 00 - 16K BASE RAM
		4913	; 01 - 32K BASE RAM
		4914	; 10 - 48K BASE RAM
		4915	; 11 - 64K BASE RAM
		4916	; PORT 62 BITS 3-0 INDICATE AMOUNT OF I/O RAM IN 32K INCREMENTS
		4917	; E.G., 0000 - NO RAM IN I/O CHANNEL
		4918	; 0010 - 64K RAM IN I/O CHANNEL, ETC.
		4919	; OUTPUT
		4920	; (AX) = NUMBER OF CONTIGUOUS 1K BLOCKS OF MEMORY
		4921	;-----
		4922	ASSUME CS:CODE,DS:DATA
F841		4923	MEMORY_SIZE_DETERMINE PROC FAR
F841 FB		4924	STI ; INTERRUPTS BACK ON
F842 1E		4925	PUSH DS ; SAVE SEGMENT
F843 B84000	R	4926	MOV AX,DATA ; ESTABLISH ADDRESSING
F846 8ED8		4927	MOV DS,AX
F848 A11300	R	4928	MOV AX,MEMORY_SIZE ; GET VALUE
F84B 1F		4929	POP DS ; RECOVER SEGMENT
F84C CF		4930	IRET ; RETURN TO CALLER
		4931	MEMORY_SIZE_DETERMINE ENDP
		4932	;--- INT 11 -----
		4933	; EQUIPMENT DETERMINATION
		4934	; THIS ROUTINE ATTEMPTS TO DETERMINE WHAT OPTIONAL
		4935	; DEVICES ARE ATTACHED TO THE SYSTEM.
		4936	; INPUT
		4937	; NO REGISTERS
		4938	; THE EQUIP_FLAG VARIABLE IS SET DURING THE POWER ON DIAGNOSTICS
		4939	; USING THE FOLLOWING HARDWARE ASSUMPTIONS:
		4940	; PORT 60 = LOW ORDER BYTE OF EQUIPMENT
		4941	; PORT 3FA = INTERRUPT ID REGISTER OF 8250
		4942	; BITS 7-3 ARE ALWAYS 0
		4943	; PORT 37B = OUTPUT PORT OF PRINTER -- 8255 PORT THAT
		4944	; CAN BE READ AS WELL AS WRITTEN
		4945	; OUTPUT
		4946	; (AX) IS SET, BIT SIGNIFICANT, TO INDICATE ATTACHED I/O
		4947	; BIT 15,14 = NUMBER OF PRINTERS ATTACHED
		4948	; BIT 13 NOT USED
		4949	; BIT 12 = GAME I/O ATTACHED
		4950	; BIT 11,10,9 = NUMBER OF RS232 CARDS ATTACHED
		4951	; BIT 8 UNUSED
		4952	; BIT 7,6 = NUMBER OF DISKETTE DRIVES
		4953	; 00=1, 01=2, 10=3, 11=4 ONLY IF BIT 0 = 1
		4954	; BIT 5,4 = INITIAL VIDEO MODE
		4955	; 00 - UNUSED
		4956	; 01 - 40X25 BW USING COLOR CARD
		4957	; 10 - 80X25 BW USING COLOR CARD
		4958	; 11 - 80X25 BW USING BW CARD
		4959	; BIT 3,2 = PLANAR RAM SIZE (00=16K,01=32K,10=48K,11=64K)
		4960	; BIT 1 NOT USED
		4961	; BIT 0 = IPL FROM DISKETTE -- THIS BIT INDICATES THAT THERE ARE DISKETTE
		4962	; DRIVES ON THE SYSTEM
		4963	;
		4964	; NO OTHER REGISTERS AFFECTED
		4965	;-----
		4966	ASSUME CS:CODE,DS:DATA

LOC	OBJ	LINE	SOURCE
F84D		4967	EQUIPMENT PROC FAR
F84D FB		4968	STI ; INTERRUPTS BACK ON
F84E 1E		4969	PUSH DS ; SAVE SEGMENT REGISTER
F84F B84000	R	4970	MOV AX,DATA ; ESTABLISH ADDRESSING
F852 8ED8		4971	MOV DS,AX
F854 A11000	R	4972	MOV AX,EQUIP_FLAG ; GET THE CURRENT SETTINGS
F857 1F		4973	POP DS ; RECOVER SEGMENT
F858 CF		4974	IRET ; RETURN TO CALLER
		4975	EQUIPMENT ENDP
		4976	INT 15
		4977	CASSETTE I/O
		4978	(AH) = 0 TURN CASSETTE MOTOR ON
		4979	(AH) = 1 TURN CASSETTE MOTOR OFF
		4980	(AH) = 2 READ 1 OR MORE 256 BYTE BLOCKS FROM CASSETTE
		4981	(ES,BX) = POINTER TO DATA BUFFER
		4982	(CX) = COUNT OF BYTES TO READ
		4983	ON EXIT:
		4984	(ES,BX) = POINTER TO LAST BYTE READ + 1
		4985	(DX) = COUNT OF BYTES ACTUALLY READ
		4986	(CY) = 0 IF NO ERROR OCCURRED
		4987	= 1 IF ERROR OCCURRED
		4988	(AH) = ERROR RETURN IF (CY)= 1
		4989	= 01 IF CRC ERROR WAS DETECTED
		4990	= 02 IF DATA TRANSITIONS ARE LOST
		4991	= 04 IF NO DATA WAS FOUND
		4992	(AH) = 3 WRITE 1 OR MORE 256 BYTE BLOCKS TO CASSETTE
		4993	(ES,BX) = POINTER TO DATA BUFFER
		4994	(CX) = COUNT OF BYTES TO WRITE
		4995	ON EXIT:
		4996	(EX,BX) = POINTER TO LAST BYTE WRITTEN + 1
		4997	(CX) = 0
		4998	(AH) = ANY OTHER THAN ABOVE VALUES CAUSES (CY)= 1
		4999	AND (AH)= 80 TO BE RETURNED (INVALID COMMAND).
		5000	-----
		5001	ASSUME DS:DATA, ES:NOTHING,SS:NOTHING,CS:CODE
F859		5002	CASSETTE_IO PROC FAR
F859 FB		5003	STI ; INTERRUPTS BACK ON
F85A 1E		5004	PUSH DS ; ESTABLISH ADDRESSING TO DATA
F85B 50		5005	PUSH AX
F85C B84000	R	5006	MOV AX, DATA
F85F 8ED8		5007	MOV DS, AX
F861 802671007F	R	5008	AND BIOS_BREAK, 7FH ; MAKE SURE BREAK FLAG IS OFF
F866 58		5009	POP AX
F867 E80400		5010	CALL W1 ; CASSETTE_IO_CONT
F86A 1F		5011	POP DS
F86B CA0200		5012	RET 2 ; INTERRUPT RETURN
		5013	CASSETTE_IO ENDP
F86E		5014	W1 PROC NEAR
		5015	-----
		5016	PURPOSE:
		5017	TO CALL APPROPRIATE ROUTINE DEPENDING ON REG AH
		5018	
		5019	AH ROUTINE
		5020	-----
		5021	0 MOTOR ON
		5022	1 MOTOR OFF
		5023	2 READ CASSETTE BLOCK
		5024	3 WRITE CASSETTE BLOCK
		5025	-----
		5026	
F86E 0AE4		5027	OR AH,AH ;TURN ON MOTOR?
F870 7413		5028	JZ MOTOR_ON ;YES, DO IT
F872 FECC		5029	DEC AH ;TURN OFF MOTOR?
F874 7418		5030	JZ MOTOR_OFF ;YES, DO IT
F876 FECC		5031	DEC AH ;READ CASSETTE BLOCK?
F878 741A		5032	JZ READ_BLOCK ;YES, DO IT
F87A FECC		5033	DEC AH ;WRITE CASSETTE BLOCK?
F87C 7503		5034	JNZ W2 ; NOT_DEFINED
F87E E92701		5035	JMP WRITE_BLOCK ;YES, DO IT
		5036	
F881		5037	W2: ;COMMAND NOT DEFINED
F881 B480		5038	MOV AH,080H ;ERROR, UNDEFINED OPERATION
F883 F9		5039	STC ;ERROR FLAG
F884 C3		5040	RET
		5041	W1 ENDP
		5042	

LOC	OBJ	LINE	SOURCE
F885		5043	MOTOR_ON PROC NEAR
		5044	-----
		5045	; PURPOSE:
		5046	; TO TURN ON CASSETTE MOTOR
		5047	-----
F885 E461		5048	IN AL,PORT_B ;READ CASSETTE OUTPUT
F887 24F7		5049	AND AL,NOT 08H ; CLEAR BIT TO TURN ON MOTOR
F889 E661		5050	W3: OUT PORT_B,AL ;WRITE IT OUT
F88B 2AE4		5051	SUB AH,AH ;CLEAR AH
F88D C3		5052	RET
		5053	MOTOR_ON ENDP
		5054	
F88E		5055	MOTOR_OFF PROC NEAR
		5056	-----
		5057	; PURPOSE:
		5058	; TO TURN CASSETTE MOTOR OFF
		5059	-----
F88E E461		5060	IN AL,PORT_B ;READ CASSETTE OUTPUT
F890 0C08		5061	OR AL,08H ; SET BIT TO TURN OFF
F892 EBF5		5062	JMP W3 ;WRITE IT, CLEAR ERROR, RETURN
		5063	MOTOR_OFF ENDP
F894		5064	READ_BLOCK PROC NEAR
		5065	-----
		5066	; PURPOSE:
		5067	; TO READ 1 OR MORE 256 BYTE BLOCKS FROM CASSETTE
		5068	;
		5069	; ON ENTRY:
		5070	; ES IS SEGMENT FOR MEMORY BUFFER (FOR COMPACT CODE)
		5071	; BX POINTS TO START OF MEMORY BUFFER
		5072	; CX CONTAINS NUMBER OF BYTES TO READ
		5073	; ON EXIT:
		5074	; BX POINTS 1 BYTE PAST LAST BYTE PUT IN MEM
		5075	; CX CONTAINS DECREMENTED BYTE COUNT
		5076	; DX CONTAINS NUMBER OF BYTES ACTUALLY READ
		5077	;
		5078	; CARRY FLAG IS CLEAR IF NO ERROR DETECTED
		5079	; CARRY FLAG IS SET IF CRC ERROR DETECTED
		5080	-----
F894 53		5081	PUSH BX ;SAVE BX
F895 51		5082	PUSH CX ;SAVE CX
F896 56		5083	PUSH SI ;SAVE SI
F897 BE0700		5084	MOV SI, 7 ; SET UP RETRY COUNT FOR LEADER
F89A E8C201		5085	CALL BEGIN_OP ; BEGIN BY STARTING MOTOR
F89D		5086	W4: ; SEARCH FOR LEADER
F89D E462		5087	IN AL,PORT_C ;GET INTIAL VALUE
F89F 2410		5088	AND AL,010H ;MASK OFF EXTRANEIOUS BITS
F8A1 A26B00	R	5089	MOV LAST_VAL,AL ;SAVE IN LOC LAST_VAL
F8A4 BA7A3F		5090	MOV DX,16250 ; # OF TRANSITIONS TO LOOK FOR
		5091	
F8A7		5092	W5: ; WAIT_FOR_EDGE
F8A7 F606710080	R	5093	TEST BIOS_BREAK, 80H ; CHECK FOR BREAK KEY
F8AC 7403		5094	JZ W6 ; JUMP IF NO BREAK KEY
F8AE E98A00		5095	JMP W17 ; JUMP IF BREAK KEY HIT
		5096	
F8B1 4A		5097	W6: DEC DX
F8B2 7503		5098	JNZ W7 ; JUMP IF BEGINNING OF LEADER
F8B4 E98400		5099	JMP W17 ; JUMP IF NO LEADER FOUND
		5100	
F8B7 E8C600		5101	W7: CALL READ_HALF_BIT ;IGNORE FIRST EDGE
F8BA E3EB		5102	JCXZ W5 ; JUMP IF NO EDGE DETECTED
F8BC BA7803		5103	MOV DX,0378H ; CHECK FOR HALF BITS
F8BF B90002		5104	MOV CX,200H ;MUST HAVE AT LEAST THIS MANY ONE SIZE
		5105	;PULSES BEFORE CHECKING FOR SYNC BIT (0)
F8C2 E421		5106	IN AL, 021H ; INTERRUPT MASK REGISTER
F8C4 0C01		5107	OR AL,1 ; DISABLE TIMER INTERRUPTS
F8C6 E621		5108	OUT 021H, AL
F8C8		5109	W8: ; SEARCH-LDR
F8C8 F606710080	R	5110	TEST BIOS_BREAK, 80H ; CHECK FOR BREAK KEY
F8CD 756C		5111	JNZ W17 ; JUMP IF BREAK KEY HIT
F8CF 51		5112	PUSH CX ;SAVE REG CX
F8D0 E8AD00		5113	CALL READ_HALF_BIT ;GET PULSE WIDTH
F8D3 0BC9		5114	OR CX, CX ; CHECK FOR TRANSITION
F8D5 59		5115	POP CX ;RESTORE ONE BIT COUNTER
F8D6 74C5		5116	JZ W4 ; JUMP IF NO TRANSITION
F8D8 3B03		5117	CMF DX,BX ;CHECK PULSE WIDTH

LOC OBJ	LINE	SOURCE
F80A E304	5118	JCXZ W9 ;IF CX=0 THEN WE CAN LOOK
	5119	;FOR SYNC BIT (0)
F80C 73BF	5120	JNC W4 ; JUMP IF ZERO BIT (NOT GOOD LEADER)
F80E E2E8	5121	LOOP W8 ;DEC CX AND READ ANOTHER HALF ONE BIT
F8E0	5122	W9: ; FIND-SYNC
F8E0 72E6	5123	JC W8 ; JUMP IF ONE BIT (STILL LEADER)
	5124	
	5125	; A SYNCH BIT HAS BEEN FOUND. READ SYN CHARACTER:
	5126	
F8E2 E89B00	5127	CALL READ_HALF_BIT ;SKIP OTHER HALF OF SYNC BIT (0)
F8E5 E86A00	5128	CALL READ_BYTE ; READ SYN BYTE
F8E8 3C16	5129	CMP AL, 16H ; SYNCHRONIZATION CHARACTER
F8EA 7549	5130	JNE W16 ; JUMP IF BAD LEADER FOUND.
	5131	
	5132	;----- GOOD CRC SO READ DATA BLOCK(S)
F8EC 5E	5133	POP SI ; RESTORE REGS
F8ED 59	5134	POP CX
F8EE 5B	5135	POP BX
	5136	;-----
	5137	; READ 1 OR MORE 256 BYTE BLOCKS FROM CASSETTE
	5138	;
	5139	; ON ENTRY:
	5140	; ES IS SEGMENT FOR MEMORY BUFFER (FOR COMPACT CODE)
	5141	; BX POINTS TO START OF MEMORY BUFFER
	5142	; CX CONTAINS NUMBER OF BYTES TO READ
	5143	; ON EXIT:
	5144	; BX POINTS 1 BYTE PAST LAST BYTE PUT IN MEM
	5145	; CX CONTAINS DECREMENTED BYTE COUNT
	5146	; DX CONTAINS NUMBER OF BYTES ACTUALLY READ
	5147	;-----
F8EF 51	5148	PUSH CX ;SAVE BYTE COUNT
F8F0	5149	W10: ;COME HERE BEFORE EACH
	5150	;256 BYTE BLOCK IS READ
F8F0 C7066900FFFF R	5151	MOV CRC_REG,0FFFFH ;INIT CRC REG
F8F6 BA0001	5152	MOV DX,256 ;SET DX TO DATA BLOCK SIZE
F8F9	5153	W11: ;RD_BLK
F8F9 F606710080 R	5154	TEST BIOS_BREAK, 80H ; CHECK FOR BREAK KEY
F8FE 7523	5155	JNZ W13 ; JUMP IF BREAK KEY HIT
F900 E84F00	5156	CALL READ_BYTE ;READ BYTE FROM CASSETTE
F903 721E	5157	JC W13 ;CY SET INDICATES NO DATA TRANSITIONS
F905 E305	5158	JCXZ W12 ;IF WE'VE ALREADY REACHED
	5159	;END OF MEMORY BUFFER
	5160	;SKIP REST OF BLOCK
F907 268807	5161	MOV ES:[BX],AL ;STORE DATA BYTE AT BYTE PTR
F90A 43	5162	INC BX ;INC BUFFER PTR
F90B 49	5163	DEC CX ;DEC BYTE COUNTER
F90C	5164	W12: ; LOOP UNTIL DATA BLOCK HAS BEEN READ FROM CASSETTE.
F90C 4A	5165	DEC DX ;DEC BLOCK CNT
F90D 7FEA	5166	JG W11 ; RD_BLK
F90F E84000	5167	CALL READ_BYTE ;NOW READ TWO CRC BYTES
F912 E83D00	5168	CALL READ_BYTE
F915 2AE4	5169	SUB AH,AH ;CLEAR AH
F917 813E6900F0 R	5170	CMP CRC_REG,100FH ;IS THE CRC CORRECT
F91D 7506	5171	JNE W14 ;IF NOT EQUAL CRC IS BAD
F91F E306	5172	JCXZ W15 ;IF BYTE COUNT IS ZERO
	5173	;THEN WE HAVE READ ENOUGH
	5174	;SO WE WILL EXIT
F921 EBCD	5175	JMP W10 ;STILL MORE, SO READ ANOTHER BLOCK
F923	5176	W13: ;MISSING-DATA
	5177	;NO DATA TRANSITIONS SO
F923 B401	5178	MOV AH,01H ;SET AH=02 TO INDICATE
	5179	;DATA TIMEOUT
F925	5180	W14: ; BAD-CRC
F925 FEC4	5181	INC AH ;EXIT EARLY ON ERROR
	5182	;SET AH=01 TO INDICATE CRC ERROR
F927	5183	W15: ; RD-BLK-EX
F927 5A	5184	POP DX ;CALCULATE COUNT OF
F928 2B01	5185	SUB DX,CX ;DATA BYTES ACTUALLY READ
	5186	;RETURN COUNT IN REG DX
F92A 50	5187	PUSH AX ;SAVE AX (RET CODE)
F92B F6C403	5188	TEST AH, 03H ; CHECK FOR ERRORS
F92E 7513	5189	JNZ W18 ; JUMP IF ERROR DETECTED
F930 E81F00	5190	CALL READ_BYTE ;READ TRAILER
F933 EB0E	5191	JMP SHORT W18 ;SKIP TO TURN OFF MOTOR
F935	5192	W16: ; BAD-LEADER
F935 4E	5193	DEC SI ;CHECK RETRIES

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LOC	OBJ	LINE	SOURCE
F97E EBF9		5268	JMP W20 ; RD_BYT_EX
		5269	READ_BYTE ENDP
		5270	;
F980		5271	READ_HALF_BIT PROC NEAR
		5272	; PURPOSE:
		5273	; TO COMPUTE TIME TILL NEXT DATA
		5274	; TRANSITION (EDGE)
		5275	;
		5276	; ON ENTRY:
		5277	; EDGE_CNT CONTAINS LAST EDGE COUNT
		5278	;
		5279	; ON EXIT:
		5280	; AX CONTAINS OLD LAST EDGE COUNT
		5281	; BX CONTAINS PULSE WIDTH (HALF BIT)
		5282	;
F980 B96400		5283	MOV CX, 100 ; SET TIME TO WAIT FOR BIT
F983 8A26B00	R	5284	MOV AH, LAST_VAL ; GET PRESENT INPUT VALUE
F987		5285	W22: ; RD-H-BIT
F987 E462		5286	IN AL, PORT_C ; INPUT DATA BIT
F989 2410		5287	AND AL, 010H ; MASK OFF EXTRANEUS BITS
F98B 3AC4		5288	CMP AL, AH ; SAME AS BEFORE?
F98D E1F8		5289	LOOPE W22 ; LOOP TILL IT CHANGES
F98F A26B00	R	5290	MOV LAST_VAL, AL ; UPDATE LAST_VAL WITH NEW VALUE
F992 B000		5291	MOV AL, 0 ; READ TIMER'S COUNTER COMMAND
F994 E643		5292	OUT TIM_CTL, AL ; LATCH COUNTER
F996 E440		5293	IN AL, TIMER0 ; GET LS BYTE
F998 8AE0		5294	MOV AH, AL ; SAVE IN AH
F99A E440		5295	IN AL, TIMER0 ; GET HS BYTE
F99C 86C4		5296	XCHG AL, AH ; XCHG AL, AH
F99E 8B1E700	R	5297	MOV BX, EDGE_CNT ; BX GETS LAST EDGE COUNT
F9A2 2BD8		5298	SUB BX, AX ; SET BX EQUAL TO HALF BIT PERIOD
F9A4 A36700	R	5299	MOV EDGE_CNT, AX ; UPDATE EDGE COUNT;
F9A7 C3		5300	RET
		5301	READ_HALF_BIT ENDP
		5302	;
F9A8		5303	WRITE_BLOCK PROC NEAR
		5304	;
		5305	; WRITE 1 OR MORE 256 BYTE BLOCKS TO CASSETTE.
		5306	; THE DATA IS PADDED TO FILL OUT THE LAST 256 BYTE BLOCK.
		5307	;
		5308	; ON ENTRY:
		5309	; BX POINTS TO MEMORY BUFFER ADDRESS
		5310	; CX CONTAINS NUMBER OF BYTES TO WRITE
		5311	;
		5312	; ON EXIT:
		5313	; BX POINTS 1 BYTE PAST LAST BYTE WRITTEN TO CASSETTE
		5314	; CX IS ZERO
		5315	;
F9A8 53		5316	PUSH BX
F9A9 51		5317	PUSH CX
F9AA E461		5318	IN AL, PORT_B ; DISABLE SPEAKER
F9AC 24F0		5319	AND AL, NOT 02H
F9AE 0C01		5320	OR AL, 01H ; ENABLE TIMER
F9B0 E661		5321	OUT PORT_B, AL
F9B2 B0B6		5322	MOV AL, 0B6H ; SET UP TIMER -- MODE 3 SQUARE WAVE
F9B4 E643		5323	OUT TIM_CTL, AL
F9B6 E8A600		5324	CALL BEGIN_OP ; START MOTOR AND DELAY
F9B9 B8A004		5325	MOV AX, 1184 ; SET NORMAL BIT SIZE
F9BC E88500		5326	CALL W31 ; SET_TIMER
F9BF B90008		5327	MOV CX, 0800H ; SET CX FOR LEADER BYTE COUNT
F9C2		5328	W23: ; WRITE LEADER
F9C2 F9		5329	STC ; WRITE ONE BITS
F9C3 E86800		5330	CALL WRITE_BIT ;
F9C6 E2FA		5331	LOOP W23 ; LOOP 'TIL LEADER IS WRITTEN
F9C8 F8		5332	CLC ; WRITE SYNC BIT (0)
F9C9 E86200		5333	CALL WRITE_BIT
F9CC 59		5334	POP CX ; RESTORE REGS CX, BX
F9CD 5B		5335	POP BX
F9CE B016		5336	MOV AL, 16H ; WRITE SYN CHARACTER
F9D0 E84400		5337	CALL WRITE_BYTE ;

LOC	OBJ	LINE	SOURCE
		5338	;------
		5339	; WRITE 1 OR MORE 256 BYTE BLOCKS TO CASSETTE
		5340	;
		5341	; ON ENTRY:
		5342	; BX POINTS TO MEMORY BUFFER ADDRESS
		5343	; CX CONTAINS NUMBER OF BYTES TO WRITE
		5344	;
		5345	; ON EXIT:
		5346	; BX POINTS 1 BYTE PAST LAST BYTE WRITTEN TO CASSETTE
		5347	; CX IS ZERO
		5348	;------
F903		5349	WR_BLOCK:
F903 C7066900FFFF R		5350	MOV CRC_REG,0FFFFH ;INIT CRC
F909 BA0001		5351	MOV DX,256 ;FOR 256 BYTES
F90C		5352	W24: MOV ; WR-BLK
F90C 268A07		5353	MOV AL,ES:[BX] ;READ BYTE FROM MEM
F90F E83500		5354	CALL WRITE_BYTE ;WRITE IT TO CASSETTE
F9E2 E302		5355	JCZX W25 ;UNLESS CX=0, ADVANCE PTRS & DEC COUNT
F9E4 43		5356	INC BX ;INC BUFFER POINTER
F9E5 49		5357	DEC CX ;DEC BYTE COUNTER
F9E6		5358	W25: ; SKIP-ADV
F9E6 4A		5359	DEC DX ;DEC BLOCK CNT
F9E7 7FF3		5360	JG W24 ;LOOP TILL 256 BYTE BLOCK
		5361	; IS WRITTEN TO TAPE
		5362	;------ WRITE CRC -----
		5363	; WRITE 1'S COMPLEMENT OF CRC REG TO CASSETTE
		5364	; WHICH IS CHECKED FOR CORRECTNESS WHEN THE BLOCK IS READ
		5365	;
		5366	; REG AX IS MODIFIED
		5367	;------
F9E9 A16900 R		5368	MOV AX,CRC_REG ;WRITE THE ONE'S COMPLEMENT OF THE
		5369	; TWO BYTE CRC TO TAPE
F9EC F7D0		5370	NOT AX ;FOR 1'S COMPLEMENT
F9EE 50		5371	PUSH AX ;SAVE IT
F9EF 86E0		5372	XCHG AH,AL ;WRITE MS BYTE FIRST
F9F1 E82300		5373	CALL WRITE_BYTE ;WRITE IT
F9F4 50		5374	POP AX ;GET IT BACK
F9F5 E81F00		5375	CALL WRITE_BYTE ;NOW WRITE LS BYTE
F9F8 0BC9		5376	OR CX,CX ;IS BYTE COUNT EXHAUSTED?
F9FA 7507		5377	JNZ WR_BLOCK ;JUMP IF NOT DONE YET
F9FC 51		5378	PUSH CX ;SAVE REG CX
F9FD B92000		5379	MOV CX, 32 ;WRITE OUT TRAILER BITS
FA00		5380	W26: ; TRAIL-LOOP
FA00 F9		5381	STC
FA01 E82A00		5382	CALL WRITE_BIT
FA04 E2FA		5383	LOOP W26 ; WRITE UNTIL TRAILER WRITTEN
FA06 59		5384	POP CX ;RESTORE REG CX
FA07 B0B0		5385	MOV AL, 0B0H ; TURN TIMER2 OFF
FA09 E643		5386	OUT TIM_CTL, AL
FA0B B00100		5387	MOV AX, 1
FA0E E83300		5388	CALL W31 ; SET_TIMER
FA11 E87AFE		5389	CALL MOTOR_OFF ;TURN MOTOR OFF
FA14 2BC0		5390	SUB AX,AX ;NO ERRORS REPORTED ON WRITE OP
FA16 C3		5391	RET ;FINISHED
		5392	WRITE_BLOCK ENDP
		5393	;------
FA17		5394	WRITE_BYTE PROC NEAR
		5395	; WRITE A BYTE TO CASSETTE.
		5396	; BYTE TO WRITE IS IN REG AL.
		5397	;------
FA17 51		5398	PUSH CX ;SAVE REGS CX,AX
FA18 50		5399	PUSH AX
FA19 8AE8		5400	MOV CH,AL ;AL-BYTE TO WRITE.
		5401	; (MS BIT WRITTEN FIRST)
FA1B B108		5402	MOV CL,8 ;FOR 8 DATA BITS IN BYTE.
		5403	; NOTE: TWO EDGES PER BIT
FA1D		5404	W27: ; DISASSEMBLE THE DATA BIT
FA1D D0D5		5405	RCL CH,1 ;ROTATE MS BIT INTO CARRY
FA1F 9C		5406	PUSHF ;SAVE FLAGS.
		5407	; NOTE: DATA BIT IS IN CARRY
FA20 E80B00		5408	CALL WRITE_BIT ;WRITE DATA BIT
FA23 9D		5409	POPF ;RESTORE CARRY FOR CRC CALC
FA24 E82400		5410	CALL CRC_GEN ;COMPUTE CRC ON DATA BIT
FA27 FEC9		5411	CL ;LOOP TILL ALL 8 BITS DONE
FA29 75F2		5412	JNZ W27 ; JUMP IF NOT DONE YET
FA2B 58		5413	POP AX ;RESTORE REGS AX,CX
FA2C 59		5414	POP CX

LOC	OBJ	LINE	SOURCE
FA2D C3		5415	RET ;WE ARE FINISHED
		5416	WRITE_BYTE ENDP
		5417	;-----
FA2E		5418	WRITE_BIT PROC NEAR
		5419	; PURPOSE:
		5420	;
		5421	; TO WRITE A DATA BIT TO CASSETTE
		5422	; CARRY FLAG CONTAINS DATA BIT
		5423	; I.E. IF SET DATA BIT IS A ONE
		5424	; IF CLEAR DATA BIT IS A ZERO
		5425	;
		5426	; NOTE: TWO EDGES ARE WRITTEN PER BIT
		5427	; ONE BIT HAS 500 USEC BETWEEN EDGES
		5428	; FOR A 1000 USEC PERIOD (1 MILLISEC)
		5429	;
		5430	; ZERO BIT HAS 250 USEC BETWEEN EDGES
		5431	; FOR A 500 USEC PERIOD (.5 MILLISEC)
		5432	; CARRY FLAG IS DATA BIT
		5433	;-----
		5434	;ASSUME IT'S A '1'
FA2E B8A004		5435	MOV AX,1184 ; SET AX TO NOMINAL ONE SIZE
FA31 7203		5436	JC W28 ; JUMP IF ONE BIT
FA33 B05002		5437	MOV AX,592 ; NO, SET TO NOMINAL ZERO SIZE
FA36		5438	W28: ; WRITE-BIT-AX
FA36 50		5439	PUSH AX ;WRITE BIT WITH PERIOD EQ TO VALUE AX
FA37		5440	W29: ;
FA37 E462		5441	IN AL,PORT_C ;INPUT TIMER_0 OUTPUT
FA39 2420		5442	AND AL,020H
FA3B 74FA		5443	JZ W29 ;LOOP TILL HIGH
FA3D		5444	W30: ;
FA3D E462		5445	IN AL,PORT_C ;NOW WAIT TILL TIMER'S OUTPUT IS LOW
FA3F 2420		5446	AND AL,020H
FA41 75FA		5447	JNZ W30
		5448	;RELOAD TIMER WITH PERIOD
		5449	;FOR NEXT DATA BIT
FA43 58		5450	POP ;RESTORE PERIOD COUNT
FA44		5451	W31: ; SET TIMER
FA44 E642		5452	OUT 042H, AL ; SET LOW BYTE OF TIMER 2
FA46 BAC4		5453	MOV AL, AH
FA48 E642		5454	OUT 042H, AL ; SET HIGH BYTE OF TIMER 2
FA4A C3		5455	RET
		5456	WRITE_BIT ENDP
		5457	;-----
FA4B		5458	CRC_GEN PROC NEAR
		5459	; UPDATE CRC REGISTER WITH NEXT DATA BIT
		5460	;
		5461	; CRC IS USED TO DETECT READ ERRORS
		5462	;
		5463	; ASSUMES DATA BIT IS IN CARRY
		5464	;
		5465	; REG AX IS MODIFIED
		5466	; FLAGS ARE MODIFIED
		5467	;-----
FA4B A16900	R	5468	MOV AX,CRC_REG
		5469	;THE FOLLOWING INSTUCTIONS
		5470	;WILL SET THE OVERFLOW FLAG
		5471	;IF CARRY AND MS BIT OF CRC
		5472	;ARE UNEQUAL
FA4E D108		5473	RCR AX,1
FA50 D100		5474	RCL AX,1
FA52 F8		5475	CLC ;CLEAR CARRY
FA53 7104		5476	JNO W32 ;SKIP IF NO OVERFLOW
		5477	; IF DATA BIT XORED WITH
		5478	; CRC REG BIT 15 IS ONE
FA55 351008		5479	XOR AX,0810H ;THEN XOR CRC REG WITH
		5480	; 0810H
FA58 F9		5481	STC ;SET CARRY
FA59		5482	W32: ;
FA59 D100		5483	RCL AX,1 ;ROTATE CARRY (DATA BIT)
		5484	;INTO CRC REG
FA5B A36900	R	5485	MOV CRC_REG,AX ;UPDATE CRC_REG
FA5E C3		5486	RET ;FINISHED
		5487	CRC_GEN ENDP
		5488	;-----
FA5F		5489	BEGIN_OP PROC NEAR ; START TAPE AND DELAY
		5490	;
		5491	;-----

LOC	OBJ	LINE	SOURCE
FA5F	E823FE	5492	CALL MOTOR_ON ;TURN ON MOTOR
FA62	B342	5493	MOV BL,42H ;DELAY FOR TAPE DRIVE
		5494	TO GET UP TO SPEED (1/2 SEC)
FA64		5495	M33:
FA64	B90007	5496	MOV CX,700H ;INNER LOOP= APPROX. 10 MILLISEC
FA67	E2FE	5497	M34: LOOP M34
FA69	FCB	5498	DEC BL
FA6B	75F7	5499	JNZ M33
FA6D	C3	5500	RET
		5501	BEGIN_OP
		5502	ENDP
		5503	; CHARACTER GENERATOR GRAPHICS FOR 320X200 AND 640X200 GRAPHICS
		5504	;
FA6E		5505	CRT_CHAR_GEN LABEL BYTE
FA6E	0000000000000000	5506	DB 000H,000H,000H,000H,000H,000H,000H,000H ; D_00
FA76	7E81A501BD99817E	5507	DB 07EH,081H,0A5H,081H,0BDH,099H,081H,07EH ; D_01
FA7E	7E7FDBFFC3E7FF7E	5508	DB 07EH,0FFH,00BH,0FFH,0C3H,0E7H,0FFH,07EH ; D_02
FA86	6CFE7E7F7C381000	5509	DB 06CH,0FEH,0FEH,0FEH,07CH,03BH,010H,000H ; D_03
FA8E	10387CFE7C381008	5510	DB 010H,03BH,07CH,0FEH,07CH,03BH,010H,00BH ; D_04
FA96	387C38FE7C387C	5511	DB 03BH,07CH,03BH,0FEH,0FEH,07CH,03BH,07CH ; D_05
FA9E	1010387CFE7C387C	5512	DB 010H,010H,03BH,07CH,0FEH,07CH,03BH,07CH ; D_06
FAAE	0000103C38180000	5513	DB 000H,000H,01BH,03CH,03CH,01BH,000H,000H ; D_07
FAA6	FFFF7C3C3E7FFFFF	5514	DB 0FFH,0FFH,0E7H,0C3H,0C3H,0E7H,0FFH,0FFH ; D_08
FAB6	003C664242663C00	5515	DB 000H,03CH,066H,042H,042H,066H,03CH,000H ; D_09
FABE	FFC3998BD99C3FF	5516	DB 0FFH,0C3H,099H,0BDH,0BDH,099H,0C3H,0FFH ; D_0A
FAC6	0F070F7DCCCC78	5517	DB 00FH,007H,00FH,07DH,0CCH,0CCH,0CCH,07BH ; D_0B
FACE	3C6666663C187E18	5518	DB 03CH,066H,066H,066H,03CH,01BH,07EH,01BH ; D_0C
FAD6	3F333F303070F0E0	5519	DB 03FH,033H,03FH,030H,030H,070H,0F0H,0E0H ; D_0D
FADE	7F637F63637E6C0	5520	DB 07FH,063H,07FH,063H,063H,067H,0E6H,0C0H ; D_0E
FAE6	995A3CE7E73C5A99	5521	DB 099H,05AH,03CH,0E7H,0E7H,03CH,05AH,099H ; D_0F
		5522	
FAEE	80E0F8FE80E08000	5523	DB 080H,0E0H,0F8H,0FEH,0F8H,0E0H,080H,000H ; D_10
FAF6	020E3EFE3E0E0200	5524	DB 002H,00EH,03EH,0FEH,03EH,00EH,002H,000H ; D_11
FAFE	183C7E18187E3C18	5525	DB 018H,03CH,07EH,018H,018H,07EH,03CH,018H ; D_12
FB06	6666666666006600	5526	DB 066H,066H,066H,066H,066H,000H,066H,000H ; D_13
FB0E	7FDB0B7B1B1B1800	5527	DB 07FH,0DBH,0DBH,07BH,01BH,01BH,01BH,000H ; D_14
FB16	3E63386C6C38CC78	5528	DB 03EH,063H,03BH,06CH,06CH,03BH,0CCH,07BH ; D_15
FB1E	0000000007E7E700	5529	DB 000H,000H,000H,000H,07EH,07EH,07EH,000H ; D_16
FB26	183C7E187E3C18FF	5530	DB 018H,03CH,07EH,018H,07EH,03CH,018H,0FFH ; D_17
FB2E	183C7E1818181800	5531	DB 018H,03CH,07EH,018H,018H,018H,018H,000H ; D_18
FB36	181818187E3C1800	5532	DB 018H,018H,018H,018H,07EH,03CH,018H,000H ; D_19
FB3E	00180CFE0C180000	5533	DB 000H,018H,00CH,0FEH,00CH,018H,000H,000H ; D_1A
FB46	003060FE60300000	5534	DB 000H,030H,060H,0FEH,060H,030H,000H,000H ; D_1B
FB4E	0000C0C0C0FE0000	5535	DB 000H,000H,0C0H,0C0H,0C0H,0FEH,000H,000H ; D_1C
FB56	002466FF66240000	5536	DB 000H,024H,066H,0FFH,066H,024H,000H,000H ; D_1D
FB5E	00183C7E7FFF0000	5537	DB 000H,018H,03CH,07EH,0FFH,0FFH,000H,000H ; D_1E
FB66	00FFFF7E3C180000	5538	DB 000H,0FFH,0FFH,07EH,03CH,018H,000H,000H ; D_1F
		5539	
FB6E	0000000000000000	5540	DB 000H,000H,000H,000H,000H,000H,000H,000H ; SP_D_20
FB76	3078783030003000	5541	DB 030H,078H,078H,030H,030H,000H,030H,000H ; D_21
FB7E	6C6C6C0000000000	5542	DB 06CH,06CH,06CH,000H,000H,000H,000H,000H ; " D_22
FB86	6C6CF66CF66C6C00	5543	DB 06CH,06CH,0FEH,06CH,0FEH,06CH,06CH,000H ; D_23
FB8E	307C0B780CF83000	5544	DB 030H,07CH,0C0H,078H,0CCH,0F8H,030H,000H ; D_24
FB96	00C6CC18306C6C00	5545	DB 000H,0C6H,0CCH,018H,030H,066H,0C6H,000H ; PER CENT D_25
FB9E	386C38760CCC7600	5546	DB 038H,06CH,038H,076H,0CCH,0CCH,076H,000H ; D_26
FBA6	606C000000000000	5547	DB 060H,060H,0C0H,000H,000H,000H,000H,000H ; D_27
FBAE	1830606060301800	5548	DB 018H,030H,060H,060H,060H,030H,018H,000H ; D_28
FB6	6030181818306000	5549	DB 060H,030H,018H,018H,018H,030H,060H,000H ; D_29
FB8E	00663CFF3C660000	5550	DB 000H,066H,03CH,0FFH,03CH,066H,000H,000H ; D_2A
FB6	003030FC30300000	5551	DB 000H,030H,030H,0FCH,030H,030H,000H,000H ; D_2B
FBCE	0000000000003060	5552	DB 000H,000H,000H,000H,000H,030H,030H,060H ; D_2C
FB06	000000FC00000000	5553	DB 000H,000H,000H,0FCH,000H,000H,000H,000H ; D_2D
FB0E	0000000000003000	5554	DB 000H,000H,000H,000H,000H,030H,030H,000H ; D_2E
FB6E	060C183060C08000	5555	DB 006H,0CCH,018H,030H,060H,0CCH,080H,000H ; D_2F
		5556	
FBEE	7CC6CEDEF6E67C00	5557	DB 07CH,0C6H,0CEH,0DEH,0F6H,0E6H,07CH,000H ; D_30
FBF6	307030303030FC00	5558	DB 030H,070H,030H,030H,030H,030H,0FCH,000H ; D_31
FBFE	78CC0C3860CCFC00	5559	DB 078H,0CCH,00CH,038H,060H,0CCH,0FCH,000H ; D_32
FC06	78CC0C380CC78000	5560	DB 078H,0CCH,00CH,038H,0CCH,0CCH,078H,000H ; D_33
FC0E	1C3C6CCFE0C1E000	5561	DB 01CH,03CH,06CH,0CCH,0FEH,0CCH,01EH,000H ; D_34
FC16	FC0CF80C0CC78000	5562	DB 0FCH,0C0H,0F8H,0CCH,0CCH,0CCH,078H,000H ; D_35
FC1E	386C0CF8CCCC7800	5563	DB 038H,060H,0C0H,0F8H,0CCH,0CCH,078H,000H ; D_36
FC26	FC0C0C1830303000	5564	DB 0FCH,0CCH,0CCH,018H,030H,030H,030H,000H ; D_37
FC2E	78CCCC78CCCC7800	5565	DB 078H,0CCH,0CCH,078H,0CCH,0CCH,078H,000H ; D_38
FC36	78CCCC7C0C187000	5566	DB 078H,0CCH,0CCH,07CH,0CCH,018H,070H,000H ; D_39
FC3E	00303000003030C0	5567	DB 000H,030H,030H,000H,000H,030H,030H,000H ; D_3A

LOC OBJ	LINE	SOURCE
FC66 0030300000303060	5568	DB 000H,030H,030H,000H,000H,030H,030H,060H ; D_3B
FC4E 183060C060301800	5569	DB 018H,030H,060H,0C0H,060H,030H,018H,000H ; < D_3C
FC56 0000FC0000F0C000	5570	DB 000H,000H,0FCH,000H,000H,0FCH,000H,000H ; > D_3D
FC5E 6030180C18306000	5571	DB 060H,030H,018H,0C0H,018H,030H,060H,000H ; > D_3E
FC66 78CC0C1830003000	5572	DB 078H,0CCH,0CCH,018H,030H,000H,030H,000H ; ? D_3F
FC6E 7CC6DEDEDEC07800	5573	
FC76 3078CCCCCCCCC000	5574	DB 07CH,0C6H,0DEH,0DEH,0DEH,0C0H,078H,000H ; A D_40
FC7E FC66667C6666FC00	5575	DB 030H,078H,0CCH,0CCH,0FCH,0CCH,0CCH,000H ; A D_41
FC86 3C66C0C0C063C000	5576	DB 0FCH,066H,066H,07CH,066H,066H,0FCH,000H ; B D_42
FC8E F8C6C6666666FC00	5577	DB 03CH,066H,0C0H,0C0H,0C0H,066H,03CH,000H ; C D_43
FC96 FE268786862FE000	5578	DB 0F8H,06CH,066H,066H,066H,06CH,0F8H,000H ; D D_44
FC9E FE268786860F0000	5579	DB 0FEH,062H,068H,078H,068H,062H,0FEH,000H ; E D_45
FC6E 7CC6DEDEDEC07800	5580	DB 0FEH,062H,068H,078H,068H,060H,0F0H,000H ; F D_46
FC6E 7CC6DEDEDEC07800	5581	DB 03CH,066H,0C0H,0C0H,0CEH,066H,03CH,000H ; G D_47
FC6E 7CC6DEDEDEC07800	5582	DB 0CCH,0CCH,0CCH,0FCH,0CCH,0CCH,0CCH,000H ; H D_48
FC6E 7830303030307800	5583	DB 078H,030H,030H,030H,030H,030H,078H,000H ; I D_49
FC6E 1E0C0C0C0C0C7800	5584	DB 01EH,0C0H,0C0H,0C0H,0CCH,0CCH,078H,000H ; J D_4A
FC6E E6666C786C6E6E00	5585	DB 0E6H,066H,06CH,078H,06CH,066H,0E6H,000H ; K D_4B
FC6E F0606060626E6E00	5586	DB 0F0H,060H,060H,060H,062H,066H,0FEH,000H ; L D_4C
FC6E C6E6E6E6C6C6C600	5587	DB 0C6H,0EEH,0FEH,0FEH,0D6H,0C6H,0C6H,000H ; M D_4D
FC6E C6E6E6E6C6C6C600	5588	DB 0C6H,0E6H,0F6H,0DEH,0CEH,0C6H,0C6H,000H ; N D_4E
FC6E 386C6C6C6C6C3800	5589	DB 038H,06CH,0C6H,0C6H,0C6H,06CH,038H,000H ; O D_4F
FC6E 386C6C6C6C6C3800	5590	
FC6E FC66667C6666F000	5591	DB 0FCH,066H,066H,07CH,060H,060H,0F0H,000H ; P D_50
FC6E 78CC0C1830003000	5592	DB 078H,0CCH,0CCH,0CCH,0CCH,078H,01CH,000H ; Q D_51
FC6E FC66667C6666E600	5593	DB 0FCH,066H,066H,07CH,06CH,066H,0E6H,000H ; R D_52
FD06 78CC0E0701CCCC7800	5594	DB 078H,0CCH,0E0H,070H,01CH,0CCH,078H,000H ; S D_53
FD0E FC84303030307800	5595	DB 0FCH,0B4H,030H,030H,030H,030H,078H,000H ; T D_54
FD16 CCCCCCCCCCCCCC00	5596	DB 0CCH,0CCH,0CCH,0CCH,0CCH,0CCH,0FCH,000H ; U D_55
FD1E CCCCCCCCCC783000	5597	DB 0CCH,0CCH,0CCH,0CCH,0CCH,078H,030H,000H ; V D_56
FD26 C6C6C6D6FE6E6E00	5598	DB 0C6H,0C6H,0C6H,0D6H,0FEH,0EEH,0C6H,000H ; W D_57
FD2E C6C6C6C38386CC600	5599	DB 0C6H,0C6H,06CH,038H,038H,0C6H,0C6H,000H ; X D_58
FD36 CCCCCC7830307800	5600	DB 0CCH,0CCH,0CCH,078H,030H,030H,078H,000H ; Y D_59
FD3E FEC68C183266FE00	5601	DB 0FEH,0C6H,08CH,018H,032H,066H,0FEH,000H ; Z D_5A
FD46 7860606060607800	5602	DB 078H,060H,060H,060H,060H,060H,078H,000H ; I D_5B
FD4E C06030180C060200	5603	DB 0C0H,060H,030H,018H,00CH,066H,002H,000H ; BACKSLASH D_5C
FD56 7818181818187800	5604	DB 078H,018H,018H,018H,018H,018H,078H,000H ; I D_5D
FD5E 10386CC60000000000	5605	DB 010H,038H,06CH,0C6H,000H,000H,000H,000H ; CIRCUMFLEX D_5E
FD66 00000000000000FF	5606	DB 000H,000H,000H,000H,000H,000H,0FFH ; _ D_5F
FD66 00000000000000FF	5607	
FD6E 3030180000000000	5608	DB 030H,030H,018H,000H,000H,000H,000H ; D_60
FD76 0000780C7CC76000	5609	DB 000H,000H,078H,00CH,07CH,0CCH,076H,000H ; LOWER CASE A D_61
FD7E E060607C6666C000	5610	DB 0E0H,060H,060H,07CH,066H,066H,0DCH,000H ; L.C. B D_62
FD86 000078CC0C7C0000	5611	DB 000H,000H,078H,0CCH,0CCH,0CCH,078H,000H ; L.C. C D_63
FD8E 1C0C0C7C0C0C7600	5612	DB 01CH,0CCH,0CCH,07CH,0CCH,0CCH,076H,000H ; L.C. D D_64
FD96 000078CFC0780000	5613	DB 000H,000H,078H,0CCH,0FCH,0C0H,078H,000H ; L.C. E D_65
FD9E 386C60F06060F000	5614	DB 038H,06CH,060H,0F0H,060H,060H,0F0H,000H ; L.C. F D_66
FD6E 3030180000000000	5615	DB 000H,000H,076H,0CCH,0CCH,07CH,0CCH,0F8H ; L.C. G D_67
FD6E 3030180000000000	5616	DB 0E0H,060H,06CH,076H,066H,066H,0E6H,000H ; L.C. H D_68
FD6E 3000703030307800	5617	DB 030H,000H,070H,030H,030H,030H,078H,000H ; L.C. I D_69
FD6E 0C000C0C0C0C0C78	5618	DB 0CCH,000H,0CCH,0CCH,0CCH,0CCH,0CCH,078H ; L.C. J D_6A
FD6E E060666C786C6E00	5619	DB 0E0H,060H,066H,06CH,078H,06CH,0E6H,000H ; L.C. K D_6B
FDCE 7030303030307800	5620	DB 070H,030H,030H,030H,030H,030H,078H,000H ; L.C. L D_6C
FDDE 0000CCFE606C6000	5621	DB 000H,000H,0CCH,0FEH,0FEH,0D6H,0C6H,000H ; L.C. M D_6D
FDDE 0000F8CCCCCCCC00	5622	DB 000H,000H,0F8H,0CCH,0CCH,0CCH,0CCH,000H ; L.C. N D_6E
FD6E 000078CCCCC07800	5623	DB 000H,000H,078H,0CCH,0CCH,0CCH,078H,000H ; L.C. O D_6F
FD6E 000078CCCCC07800	5624	
FDDE 0000C66667C60F00	5625	DB 000H,000H,0DCH,066H,066H,07CH,060H,0F0H ; L.C. P D_70
FD6E 000076CCCC7C0C1E	5626	DB 000H,000H,076H,0CCH,0CCH,07CH,0CCH,01EH ; L.C. Q D_71
FDDE 0000DC76666F0000	5627	DB 000H,000H,0DCH,076H,066H,060H,0F0H,000H ; L.C. R D_72
FE06 00007C0780CF8000	5628	DB 000H,000H,07CH,0C0H,078H,00CH,0F8H,000H ; L.C. S D_73
FE0E 10307C3030341800	5629	DB 010H,030H,07CH,030H,030H,034H,018H,000H ; L.C. T D_74
FE16 0000CCCCCCCC7600	5630	DB 000H,000H,0CCH,0CCH,0CCH,0CCH,076H,000H ; L.C. U D_75
FE1E 0000CCCCC7830000	5631	DB 000H,000H,0CCH,0CCH,0CCH,078H,030H,000H ; L.C. V D_76
FE26 0000C6D6FEFE6C00	5632	DB 000H,000H,0C6H,0D6H,0FEH,0FEH,0C6H,000H ; L.C. W D_77
FE2E 0000C66C386CC600	5633	DB 000H,000H,0C6H,06CH,038H,06CH,0C6H,000H ; L.C. X D_78
FE36 0000CCCCC7C0CF80	5634	DB 000H,000H,0CCH,0CCH,0CCH,07CH,0CCH,0F8H ; L.C. Y D_79
FE3E 0000FC983064FC00	5635	DB 000H,000H,0FCH,098H,030H,064H,0FCH,000H ; L.C. Z D_7A
FE46 1C3030E030301C00	5636	DB 01CH,030H,030H,0E0H,030H,030H,01CH,000H ; D_7B
FE4E 1818180018181800	5637	DB 018H,018H,018H,000H,018H,018H,018H,000H ; D_7C
FE56 E030301C3030E000	5638	DB 0E0H,030H,030H,01CH,030H,030H,0E0H,000H ; D_7D
FE5E 76DC00000000000000	5639	DB 076H,0DCH,000H,000H,000H,000H,000H,000H ; D_7E
FE66 0010386CC6C6FE00	5640	DB 000H,010H,038H,06CH,0C6H,0C6H,0FEH,000H ; DELTA D_7F

LOC OBJ

LINE SOURCE

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5641 ;--- INT 1A -----
5642 ; TIME_OF_DAY
5643 ; THIS ROUTINE ALLOWS THE CLOCK TO BE SET/READ
5644 ;
5645 ; INPUT
5646 ; (AH) = 0 READ THE CURRENT CLOCK SETTING
5647 ; RETURNS CX = HIGH PORTION OF COUNT
5648 ; DX = LOW PORTION OF COUNT
5649 ; AL = 0 IF TIMER HAS NOT PASSED 24 HOURS SINCE LAST READ
5650 ; <0 IF ON ANOTHER DAY
5651 ; (AH) = 1 SET THE CURRENT CLOCK
5652 ; CX = HIGH PORTION OF COUNT
5653 ; DX = LOW PORTION OF COUNT
5654 ; NOTE: COUNTS OCCUR AT THE RATE OF 1193180/65536 COUNTS/SEC
5655 ; (OR ABOUT 18.2 PER SECOND -- SEE EQUATES BELOW)
5656 ;-----
5657 ASSUME CS:CODE,DS:DATA
FE6E 5658 TIME_OF_DAY PROC FAR
FE6E FB 5659 STI ; INTERRUPTS BACK ON
FE6F 1E 5660 PUSH DS ; SAVE SEGMENT
FE70 50 5661 PUSH AX ; SAVE PARAM
FE71 B84000 R 5662 MOV AX,DATA
FE74 8ED8 5663 MOV DS,AX ; ESTABLISH ADDRESSING TO VALUES
FE76 58 5664 POP AX ; GET BACK INPUT PARAM
FE77 0AE4 5665 OR AH,AH ; AH=0
FE79 7407 5666 JZ T2 ; READ_TIME
FE7B FECC 5667 DEC AH ; AH=1
FE7D 7416 5668 JZ T3 ; SET_TIME
FE7F 5669 T1: ; TOD_RETURN
FE7F FB 5670 STI ; INTERRUPTS BACK ON
FE80 1F 5671 POP DS ; RECOVER SEGMENT
FE81 CF 5672 IRET ; RETURN TO CALLER
5673
FE82 5674 T2: ; READ_TIME
FE82 FA 5675 CLI ; NO TIMER INTERRUPTS WHILE READING
FE83 A07000 R 5676 MOV AL,TIMER_OFL
FE86 C06700000 R 5677 MOV TIMER_OFL,0 ; GET OVERFLOW, AND RESET THE FLAG
FE8B 8B0E6E00 R 5678 MOV CX,TIMER_HIGH
FE8F 8B166C00 R 5679 MOV DX,TIMER_LOW
FE93 EBEA 5680 JMP T1 ; TOD_RETURN
5681
FE95 5682 T3: ; SET_TIME
FE95 FA 5683 CLI ; NO INTERRUPTS WHILE WRITING
FE96 89166C00 R 5684 MOV TIMER_LOW,DX
FE9A 890E6E00 R 5685 MOV TIMER_HIGH,CX ; SET THE TIME
FE9E C06700000 R 5686 MOV TIMER_OFL,0 ; RESET OVERFLOW
FEA3 EBD4 5687 JMP T1 ; TOD_RETURN
5688 TIME_OF_DAY ENDP
5689 ;-----
5690 ; THIS ROUTINE HANDLES THE TIMER INTERRUPT FROM
5691 ; CHANNEL 0 OF THE 8253 TIMER. INPUT FREQUENCY IS 1.19318 MHZ
5692 ; AND THE DIVISOR IS 65536, RESULTING IN APPROX. 18.2 INTERRUPTS
5693 ; EVERY SECOND.
5694 ;
5695 ; THE INTERRUPT HANDLER MAINTAINS A COUNT OF INTERRUPTS SINCE POWER
5696 ; ON TIME, WHICH MAY BE USED TO ESTABLISH TIME OF DAY.
5697 ; THE INTERRUPT HANDLER ALSO DECREMENTS THE MOTOR CONTROL COUNT
5698 ; OF THE DISKETTE, AND WHEN IT EXPIRES, WILL TURN OFF THE DISKETTE
5699 ; MOTOR, AND RESET THE MOTOR RUNNING FLAGS
5700 ; THE INTERRUPT HANDLER WILL ALSO INVOKE A USER ROUTINE THROUGH INTERRUPT
5701 ; ICH AT EVERY TIME TICK. THE USER MUST CODE A ROUTINE AND PLACE THE
5702 ; CORRECT ADDRESS IN THE VECTOR TABLE.
5703 ;-----
FEA5 5704 TIMER_INT PROC FAR
FEA5 FB 5705 STI ; INTERRUPTS BACK ON
FEA6 1E 5706 PUSH DS
FEA7 50 5707 PUSH AX
FEA8 52 5708 PUSH DX ; SAVE MACHINE STATE
FEA9 B84000 R 5709 MOV AX,DATA
FEAC 8ED8 5710 MOV DS,AX ; ESTABLISH ADDRESSABILITY
FEAE FF066C00 R 5711 INC TIMER_LOW ; INCREMENT TIME
FEB2 7504 5712 JNZ T4 ; TEST_DAY
FEB4 FF066E00 R 5713 INC TIMER_HIGH ; INCREMENT HIGH WORD OF TIME
FEB8 5714 T4: ; TEST_DAY

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LOC	OBJ	LINE	SOURCE
FEB8	833E6E0018	R 5715	CHP TIMER_HIGH,018H ; TEST FOR COUNT EQUALLING 24 HOURS
FEBD	7519	5716	JNZ T5 ; DISKETTE_CTL
FEBF	813E6C00B000	R 5717	CHP TIMER_LOW,0B0H
FEC5	7511	5718	JNZ T5 ; DISKETTE_CTL
		5719	
		5720	;----- TIMER HAS GONE 24 HOURS
		5721	
FEC7	C7066E000000	R 5722	MOV TIMER_HIGH,0
FEDC	C7066C000000	R 5723	MOV TIMER_LOW,0
FED3	C606700001	R 5724	MOV TIMER_OF,1
		5725	
		5726	;----- TEST FOR DISKETTE TIME OUT
		5727	
FED8		5728	T5: ; DISKETTE_CTL
FED8	FE0E4000	R 5729	DEC MOTOR_COUNT
FEDC	750B	5730	JNZ T6 ; RETURN IF COUNT NOT OUT
FEDE	80263F00F0	R 5731	AND MOTOR_STATUS,0F0H ; TURN OFF MOTOR RUNNING BITS
FEED	800C	5732	MOV AL,0CH
FEES	BAF203	5733	MOV DX,03F2H ; FDC CTL PORT
FEED	EE	5734	OUT DX,AL ; TURN OFF THE MOTOR
		5735	
FEED		5736	T6: ; TIMER_RET:
FEED	CD1C	5737	INT 1CH ; TRANSFER CONTROL TO A USER ROUTINE
FEED	B020	5738	MOV AL,EDI
FEED	E620	5739	OUT 020H,AL ; END OF INTERRUPT TO 8259
FEED	5A	5740	POP DX
FEED	58	5741	POP AX
FEED	1F	5742	POP DS ; RESET MACHINE STATE
FEED	CF	5743	IRET ; RETURN FROM INTERRUPT
		5744	TIMER_INT ENDP
		5745	;-----
		5746	; THESE ARE THE VECTORS WHICH ARE MOVED INTO
		5747	; THE 8086 INTERRUPT AREA DURING POWER ON
		5748	;-----
FEF3		5749	VECTOR_TABLE LABEL WORD ; VECTOR TABLE FOR MOVE TO INTERRUPTS
		5750	
FEF3	A5FE	R 5751	DW OFFSET TIMER_INT ; INTERRUPT 8
FEF5	00F0	R 5752	DW CODE
		5753	
FEF7	87E9	R 5754	DW OFFSET KB_INT ; INTERRUPT 9
FEF9	00F0	R 5755	DW CODE
		5756	
FEFB	00000000	5757	DD 0 ; INTERRUPT A
FEFF	00000000	5758	DD 0 ; INTERRUPT B
FF03	00000000	5759	DD 0 ; INTERRUPT C
FF07	00000000	5760	DD 0 ; INTERRUPT D
		5761	
FF0B	57EF	R 5762	DW OFFSET DISK_INT ; INTERRUPT E
FF0D	00F0	R 5763	DW CODE
		5764	
FF0F	00000000	5765	DD 0 ; INTERRUPT F
		5766	
FF13	65F0	R 5767	DW OFFSET VIDEO_IO ; INTERRUPT 10H
FF15	00F0	R 5768	DW CODE
		5769	
FF17	40F8	R 5770	DW OFFSET EQUIPMENT ; INTERRUPT 11H
FF19	00F0	R 5771	DW CODE
		5772	
FF1B	41F8	R 5773	DW OFFSET MEMORY_SIZE_DETERMINE ; INT 12H
FF1D	00F0	R 5774	DW CODE
		5775	
FF1F	59EC	R 5776	DW OFFSET DISKETTE_IO ; INTERRUPT 13H
FF21	00F0	R 5777	DW CODE
		5778	
FF23	39E7	R 5779	DW OFFSET RS232_IO ; INTERRUPT 14H
FF25	00F0	R 5780	DW CODE
		5781	
FF27	59F8	R 5782	DW OFFSET CASSETTE_IO ; INTERRUPT 15H
FF29	00F0	R 5783	DW CODE
		5784	
FF2B	2EE8	R 5785	DW OFFSET KEYBOARD_IO ; INTERRUPT 16H
FF2D	00F0	R 5786	DW CODE
		5787	
FF2F	D2EF	R 5788	DW OFFSET PRINTER_IO ; INTERRUPT 17H
FF31	00F0	R 5789	DW CODE
		5790	

LOC	OBJ	LINE	SOURCE
FF33	0000	5791	DW 00000H ; INTERRUPT 18H
FF35	00F6	5792	DW 0F600H ; ROM BASIC ENTRY POINT
		5793	
FF37	F2E6	R 5794	DW OFFSET BOOT_STRAP ; INTERRUPT 19H
FF39	00F0	R 5795	DW CODE
		5796	
FF3B	6EFE	R 5797	DW TIME_OF_DAY ; INTERRUPT 1AH -- TIME OF DAY
FF3D	00F0	R 5798	DW CODE
		5799	
FF3F	53FF	R 5800	DW DUMMY_RETURN ; INTERRUPT 1BH -- KEYBOARD BREAK ADDR
FF41	00F0	R 5801	DW CODE
		5802	
FF43	53FF	R 5803	DW DUMMY_RETURN ; INTERRUPT 1C -- TIMER BREAK ADDR
FF45	00F0	R 5804	DW CODE
		5805	
FF47	A4F0	R 5806	DW VIDEO_PARMS ; INTERRUPT 1D -- VIDEO PARAMETERS
FF49	00F0	R 5807	DW CODE
		5808	
FF4B	C7EF	R 5809	DW OFFSET DISK_BASE ; INTERRUPT 1E -- DISK PARMS
FF4D	00F0	R 5810	DW CODE
		5811	
FF4F	00000000	5812	DD 0 ; INTERRUPT 1F -- POINTER TO VIDEO EXT
		5813	
FF53		5814	DUMMY_RETURN:
FF53	CF	5815	IRET ; DUMMY RETURN FOR BREAK FROM KEYBOARD
		5816	;
		5817	INT 5 -----
		5818	;
		5819	THIS LOGIC WILL BE INVOKED BY INTERRUPT 05H TO PRINT
		5820	THE SCREEN. THE CURSOR POSITION AT THE TIME THIS ROUTINE
		5821	IS INVOKED WILL BE SAVED AND RESTORED UPON COMPLETION. THE
		5822	ROUTINE IS INTENDED TO RUN WITH INTERRUPTS ENABLED.
		5823	IF A SUBSEQUENT 'PRINT SCREEN KEY IS DEPRESSED DURING THE
		5824	TIME THIS ROUTINE IS PRINTING IT WILL BE IGNORED.
		5825	ADDRESS 50:0 CONTAINS THE STATUS OF THE PRINT SCREEN:
		5826	;
		5827	50:0 =0 EITHER PRINT SCREEN HAS NOT BEEN CALLED
		5828	OR UPON RETURN FROM A CALL THIS INDICATES
		5829	A SUCCESSFUL OPERATION.
		5830	;
		5831	=1 PRINT SCREEN IS IN PROGRESS
		5832	;
		5833	=377 ERROR ENCOUNTERED DURING PRINTING
		5834	-----
		5835	ASSUME CS:CODE,DS:XXDATA
		5836	
FF54		5837	PRINT_SCREEN PROC FAR
FF54	FB	5838	STI ;MUST RUN WITH INTERRUPTS ENABLED
FF55	1E	5839	PUSH DS ;MUST USE 50:0 FOR DATA AREA STORAGE
FF56	50	5840	PUSH AX
FF57	53	5841	PUSH BX
FF58	51	5842	PUSH CX ;WILL USE THIS LATER FOR CURSOR LIMITS
FF59	52	5843	PUSH DX ;WILL HOLD CURRENT CURSOR POSITION
FF5A	B85000	5844	MOV AX,XXDATA ;HEX 50
FF5D	8ED8	5845	MOV DS,AX
FF5F	803E000001	5846	CMPL STATUS_BYTE,1 ;SEE IF PRINT ALREADY IN PROGRESS
FF64	745F	5847	JZ EXIT ; JUMP IF PRINT ALREADY IN PROGRESS
FF66	C606000001	5848	MOV STATUS_BYTE,1 ;INDICATE PRINT NOW IN PROGRESS
FF6B	B40F	5849	MOV AH,15 ;WILL REQUEST THE CURRENT SCREEN MODE
FF6D	CD10	5850	INT 10H ; [AL]=MODE
		5851	;
		5852	[AH]=NUMBER COLUMNS/LINE
		5853	;
		5854	[BH]=VISUAL PAGE
		5855	*****
		5856	;
		5857	AT THIS POINT WE KNOW THE COLUMNS/LINE ARE IN
		5858	[AX] AND THE PAGE IF APPLICABLE IS IN [BH]. THE STACK
		5859	HAS DS,AX,BX,CX,DX PUSHED. [AL] HAS VIDEO MODE
		5860	;
		5861	*****
FF6F	8ACC	5862	MOV CL,AH ;WILL MAKE USE OF [CX] REGISTER TO
FF71	B519	5863	MOV CH,25 ;CONTROL ROW & COLUMNS
FF73	E85500	5864	CALL CRLF ;CARRIAGE RETURN LINE FEED ROUTINE
FF76	51	5865	PUSH CX ;SAVE SCREEN BOUNDS
FF77	B403	5866	MOV AH,3 ;WILL NOW READ THE CURSOR.
FF79	CD10	5867	INT 10H ;AND PRESERVE THE POSITION
FF7B	59	5868	POP CX ;RECALL SCREEN BOUNDS
FF7C	52	5869	PUSH DX ;RECALL [BH]=VISUAL PAGE
FF7D	3302	5870	XOR DX,DX ;WILL SET CURSOR POSITION TO [0,0]

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5066 ;*****
5067 ; THE LOOP FROM PRI10 TO THE INSTRUCTION PRIOR TO PRI20
5068 ; IS THE LOOP TO READ EACH CURSOR POSITION FROM THE SCREEN
5069 ; AND PRINT.
5070 ;*****
5071 PRI10: MOV AH,2 ;TO INDICATE CURSOR SET REQUEST
5072 INT 10H ;NEW CURSOR POSITION ESTABLISHED
5073 MOV AH,8 ;TO INDICATE READ CHARACTER
5074 INT 10H ;CHARACTER NOW IN [AL]
5075 OR AL,AL ;SEE IF VALID CHAR
5076 JNZ PRI15 ;JUMP IF VALID CHAR
5077 MOV AL,' ' ;MAKE A BLANK
5078
5079 PRI15:
5080 PUSH DX ;SAVE CURSOR POSITION
5081 XOR DX,DX ;INDICATE PRINTER 1
5082 XOR AH,AH ;TO INDICATE PRINT CHAR IN [AL]
5083 INT 17H ;PRINT THE CHARACTER
5084 POP DX ;RECALL CURSOR POSITION
5085 TEST AH, 25H ; TEST FOR PRINTER ERROR
5086 JNZ ERR10 ; JUMP IF ERROR DETECTED
5087 INC DL ;ADVANCE TO NEXT COLUMN
5088 CMP CL,DL ;SEE IF AT END OF LINE
5089 JNZ PRI10 ;IF NOT PROCEED
5090 XOR DL,DL ;BACK TO COLUMN 0
5091 MOV AH,DL ;[AH]=0
5092 PUSH DX ;SAVE NEW CURSOR POSITION
5093 CALL CRLF ; LINE FEED CARRIAGE RETURN
5094 POP DX ;RECALL CURSOR POSITION
5095 INC DH ;ADVANCE TO NEXT LINE
5096 CMP CH,DH ;FINISHED?
5097 JNZ PRI10 ;IF NOT CONTINUE
5098
5099 PRI20: POP DX ;RECALL CURSOR POSITION
5100 MOV AH,2 ;TO INDICATE CURSOR SET REQUEST
5101 INT 10H ;CURSOR POSITION RESTORED
5102 MOV STATUS_BYTE,0 ;INDICATE FINISHED
5103 JMP SHORT EXIT ;EXIT THE ROUTINE
5104
5105 ERR10: POP DX ;GET CURSOR POSITION
5106 MOV AH,2 ;TO REQUEST CURSOR SET
5107 INT 10H ;CURSOR POSITION RESTORED
5108
5109 ERR20: MOV STATUS_BYTE,0FFH ;INDICATE ERROR
5110
5111 EXIT: POP DX ;RESTORE ALL THE REGISTERS USED
5112 POP CX
5113 POP BX
5114 POP AX
5115 POP DS
5116 IRET
5117 PRINT_SCREEN ENDP
5118
5119 ;----- CARRIAGE RETURN, LINE FEED SUBROUTINE
5120
5121 CRLF PROC NEAR
5122 XOR DX,DX ;PRINTER 0
5123 XOR AH,AH ;WILL NOW SEND INITIAL LF,CR TO PRINTER
5124 MOV AL,120H ;LF
5125 INT 17H ;SEND THE LINE FEED
5126 XOR AH,AH ;NOW FOR THE CR
5127 MOV AL,150H ;CR
5128 INT 17H ;SEND THE CARRIAGE RETURN
5129 RET
5130 CRLF ENDP
5131 CODE ENDS
5132
5133 ;----- POWER ON RESET VECTOR
5134
5135 VECTOR SEGMENT AT 0FFFFH
5136
5137 ;----- POWER ON RESET
5138
5139 JMP RESET
5140
5141 DB '04/24/81' ; RELEASE MARKER
5142
5143 VECTOR ENDS
5144 ENDD

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